



Mrs. D.Durga Bhavani

Assistant Professor

Department of Electronics & communication Engineering

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Interests: VLSI, Image processing, Antennas and wave Propagation

Course Taught Previously

UG.

- Antennas and Wave Propagation
- Electromagnetic Wave theory and transmission lines
- Digital Logic Design
- Verilog HDL
- **Laboratories Handled**
- Verilog HDL lab (Dealing with simulation of codes for combinational and sequential circuits)
- VLSI lab
- Microprocessor and Microcontrollers Lab (Dealing with 8086, 8051 Micro Controller)
- Electronic Devices Lab (Designing circuits using BJT, FET & CE CC Amplifiers)

Publications

1. Published paper on “An Efficient Reversible Data Hiding Techniques for High Quality Images Using Histogram Modification Method”, International Journal of Research studies in Science, engineering and technology (IJRSSET), 2014.

Seminars, Workshops

S. No	Name of the Programme	Dates	Sponsored by
1.	Advances in Wireless Communications and Antenna Technologies for Space, Vehicular and Ground Applications	June 12,2023	Organized by WAMS, Raghu Institute of Technology (A) and Raghu Engineering

			College (A)
2.	Artificial intelligence machine learning and deeplearning with matlab and simulink	November 23-25,2022	Department Of ECE,REC in association with CAPRICOT Technologies Pvt.Ltd, Hyderabad
3.	Inculcating Universal Human Values in Technical Education	November 14-18,2022	All India Council for Technical Education (AICTE)
4.	Analog and Digital IC Design Using Cadence Design Flow	October 26-29,2022	Raghu Engineering College
5.	National Intellectual Property Awareness Mission	July 28,2022	Govt. of India, NIPAM
3.	Teaching Generation Next: A Pedagogy for Todays Learners	December 26,2015	VITS College of Engineering

Awards

1 .Achieved Gold medal in NPTEL online course System Design through Verilog.

Additional Responsibility

RESPONSIBILITIES WITHIN THE INSTITUTE:

1. Member of Women Empowerment Cell
2. Mentor for 3RD year B.Tech ECE students
3. Lab Incharge of Simulation lab